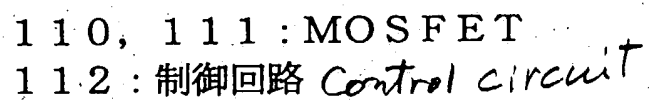
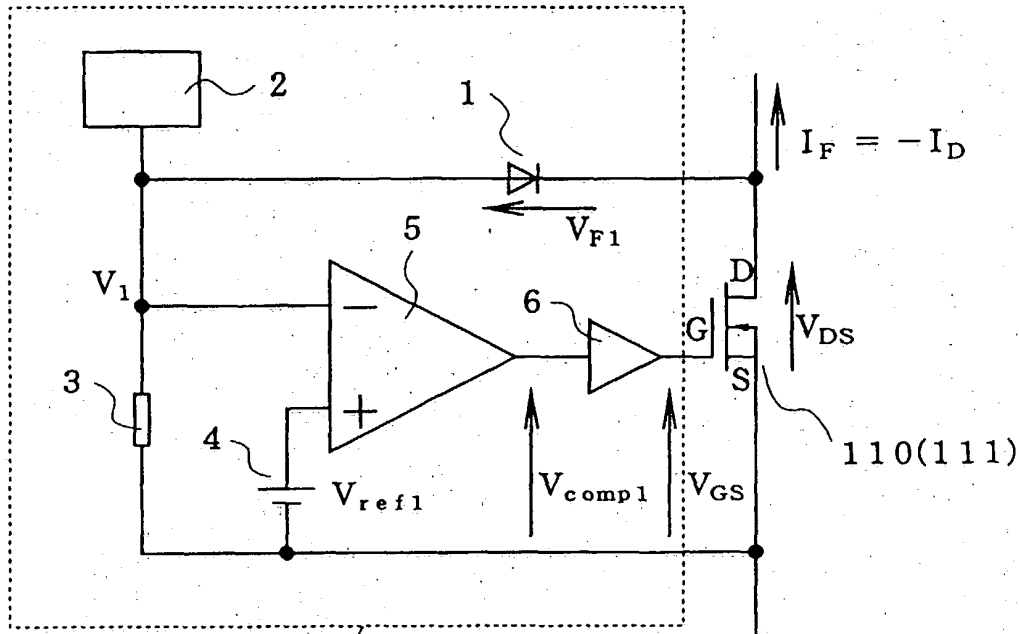


【图 1】



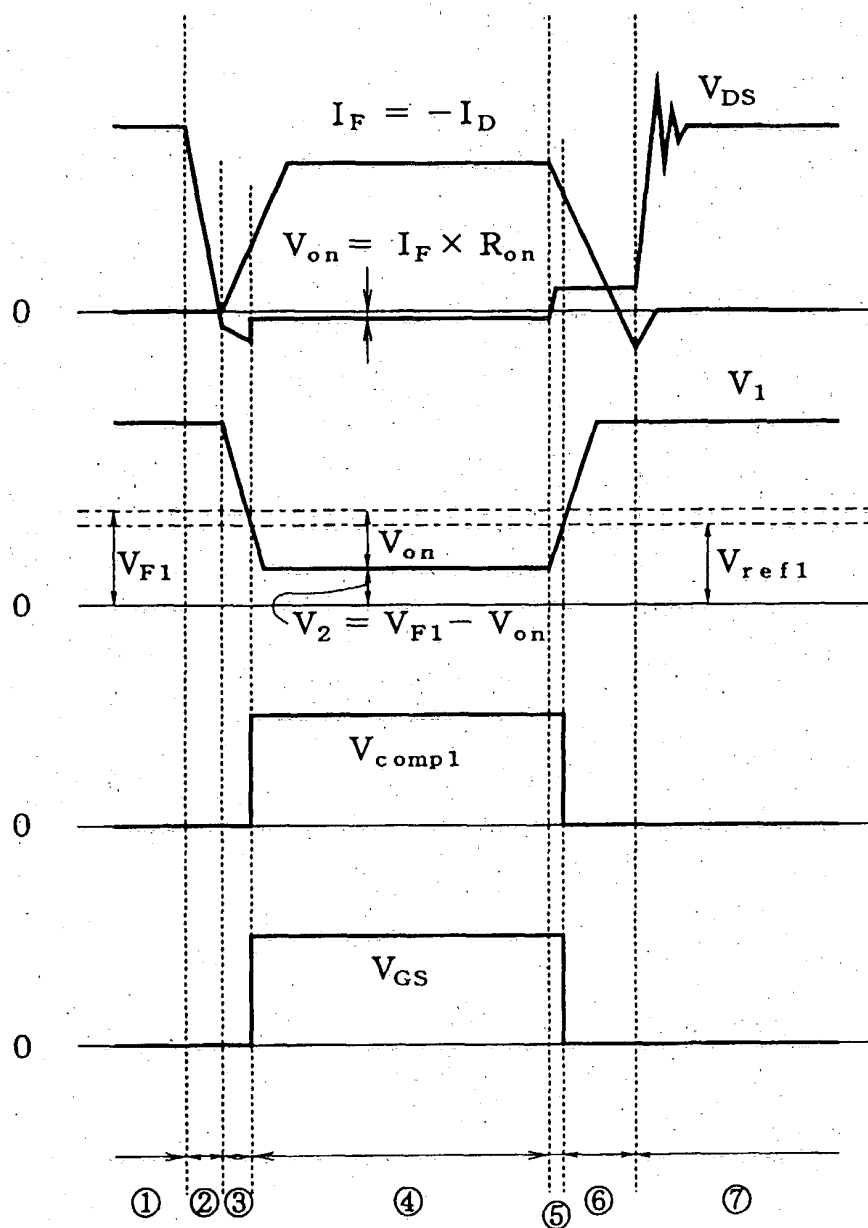
【図2】



112a

- 1 : ダイオード Diode
- 2 : 電流供給手段 Current supplying unit
- 3 : 抵抗 Resistor
- 4 : 基準電源 Reference voltage source
- 5 : 電圧比較手段 Voltage comparing unit
- 6 : ゲート駆動手段 Gate driving unit
- 112a : 制御回路 Control circuit

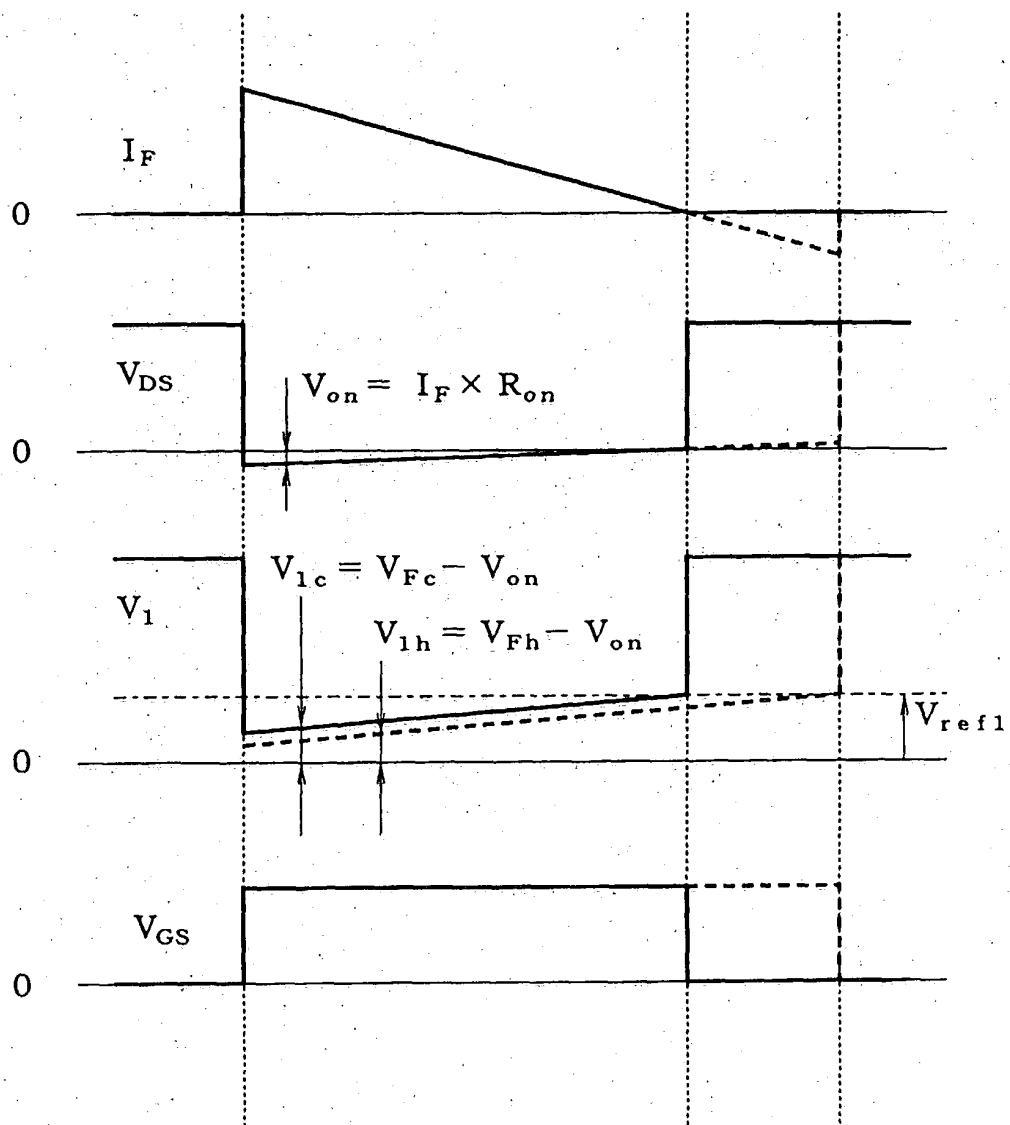
【図3】



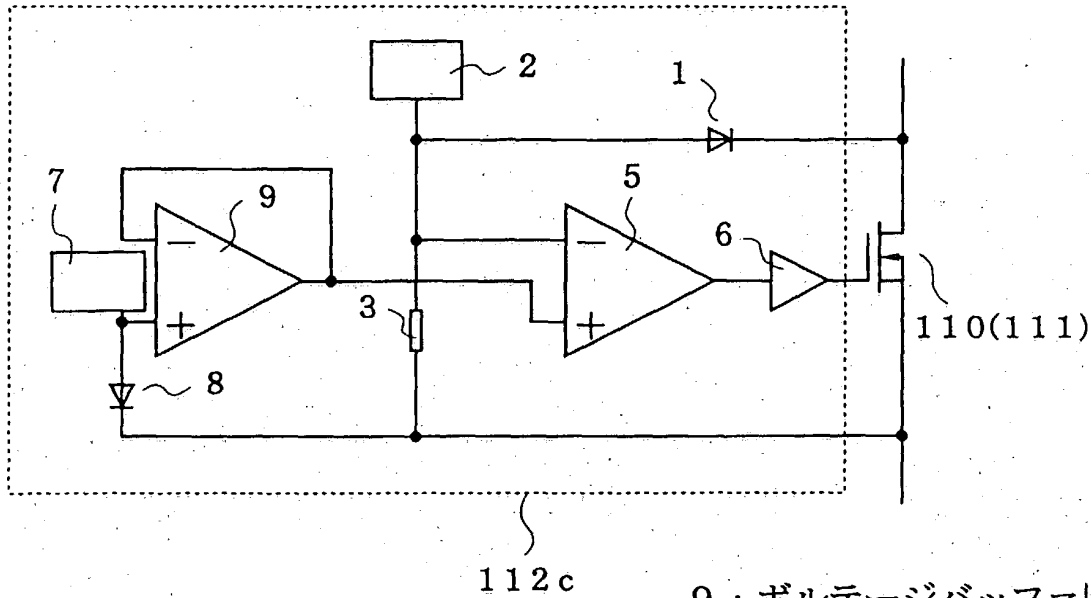
7 : 電流供給手段 Current supplying
8 : ダイオード Diode unit
112b : 制御回路 Control circuit

A graph showing the forward current I_F versus forward voltage V_F for a diode. Two curves are plotted: one for a hot junction ($T_j : \text{Hot}$) and one for a cold junction ($T_j : \text{Cold}$). The hot junction curve is steeper and shifted to the left of the cold junction curve. A horizontal dashed line at I_{F1} intersects the hot curve at V_{Fh} and the cold curve at V_{Fc} , where $V_{Fh} < V_{Fc}$.

【図6】

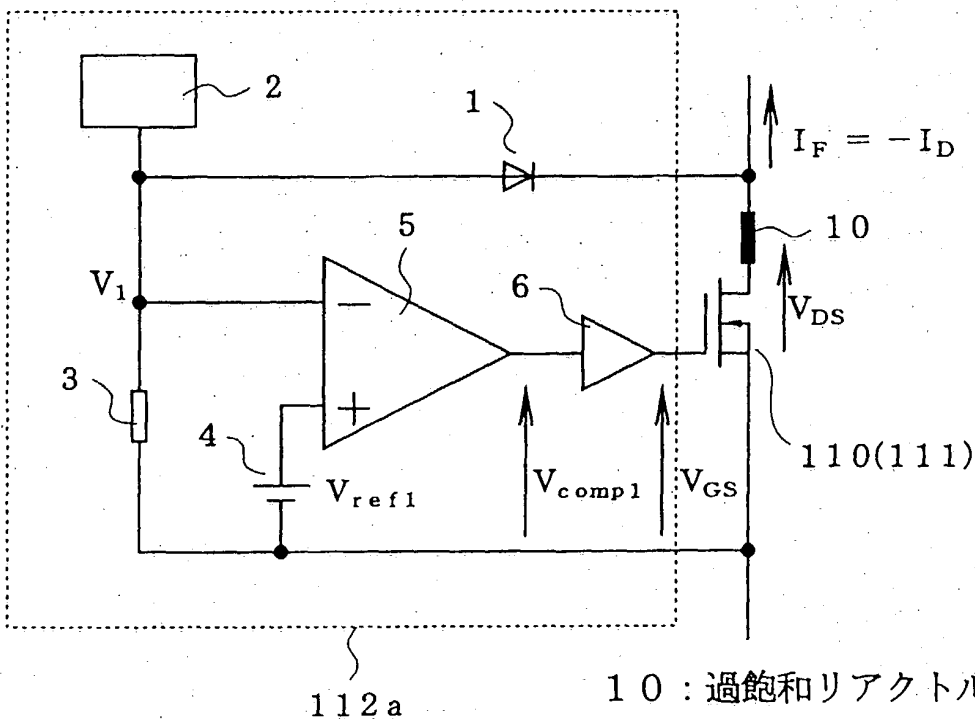


【図7】

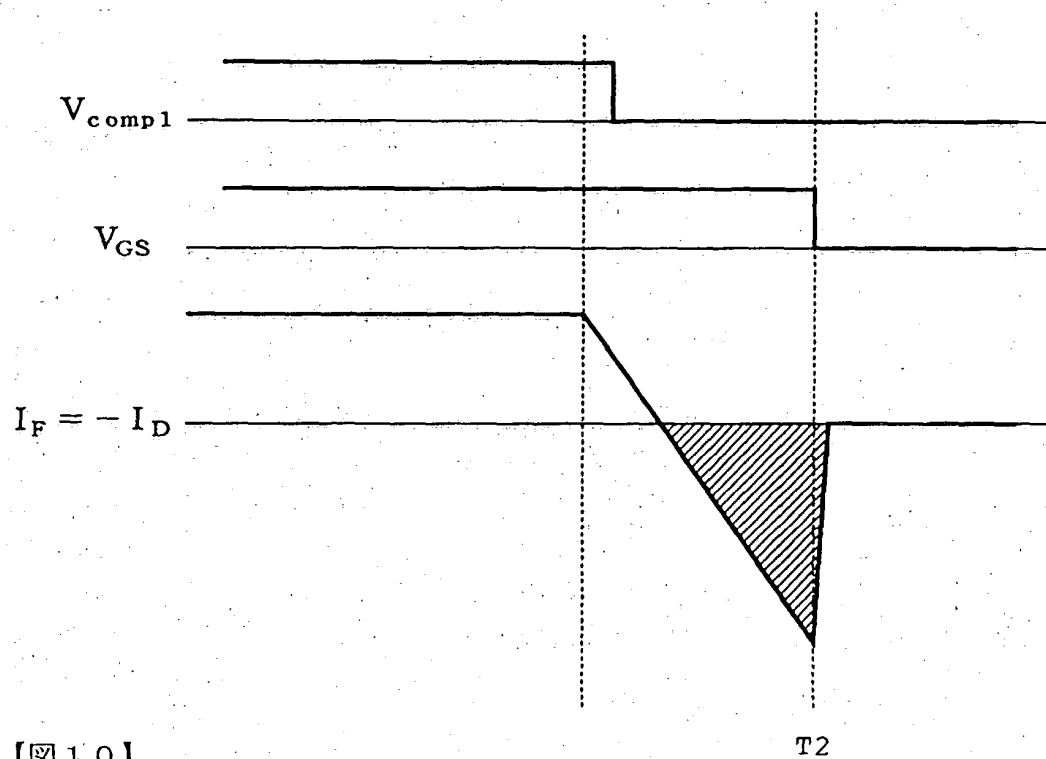


9 : ボルテージバッファ Voltage buffer
112c : 制御回路 Control circuit

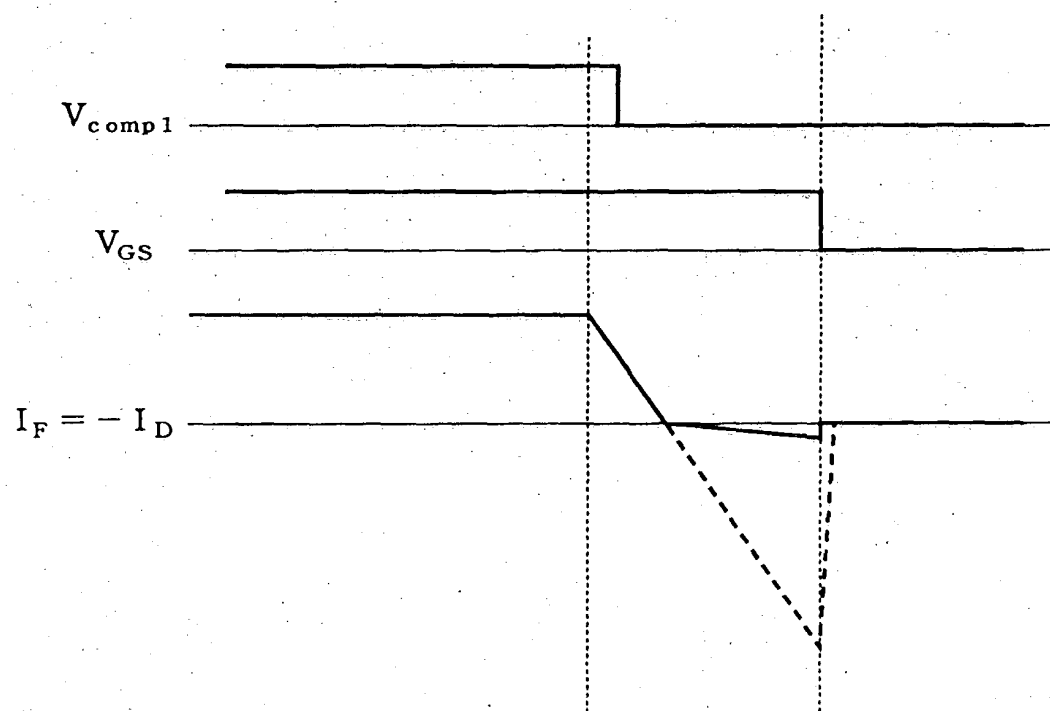
【図8】



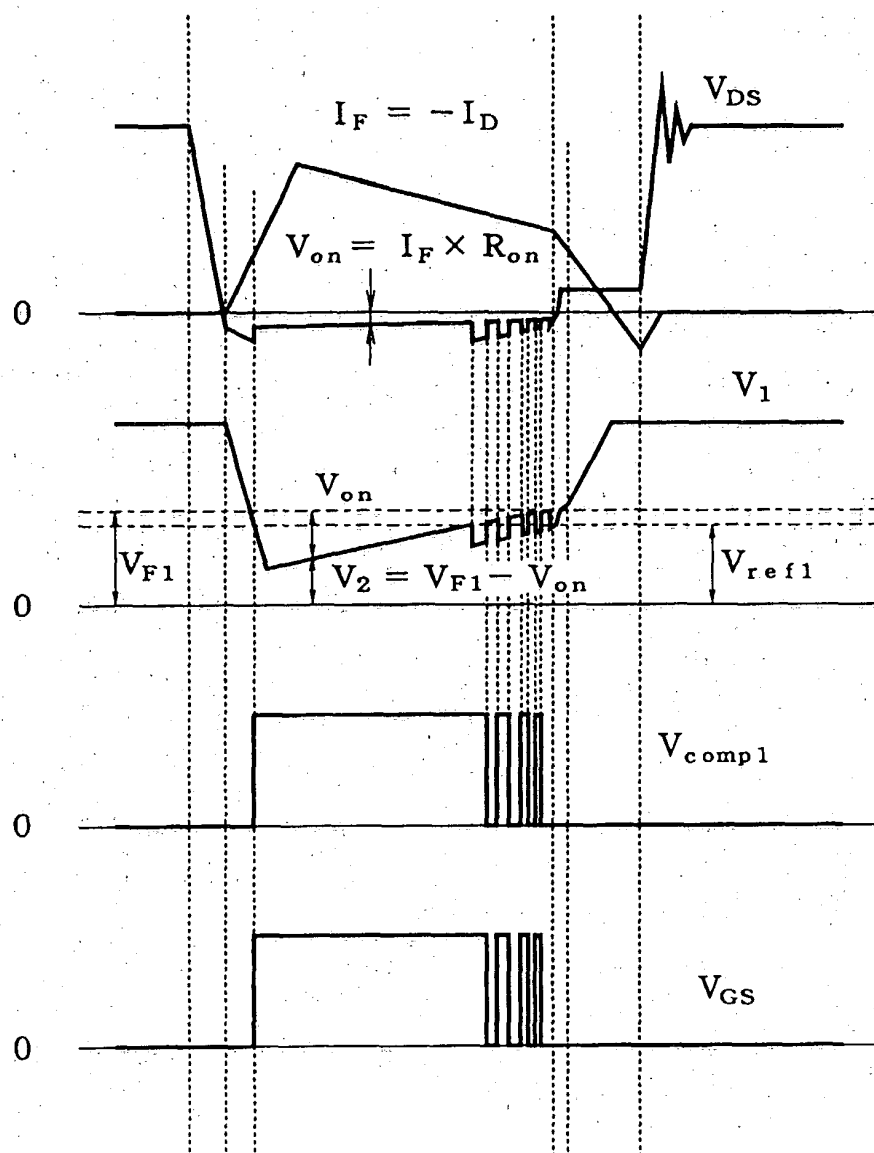
【図9】



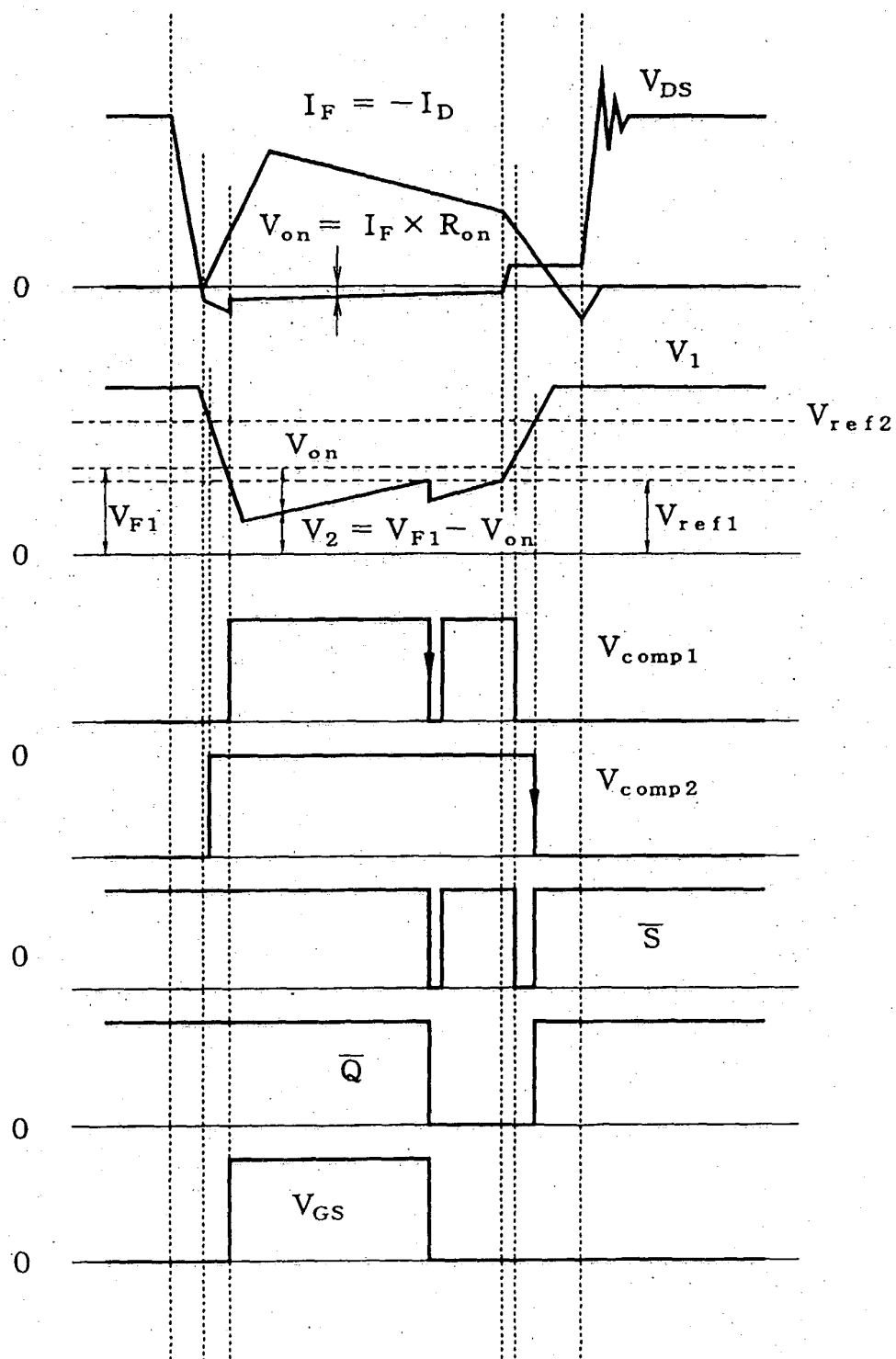
【図10】



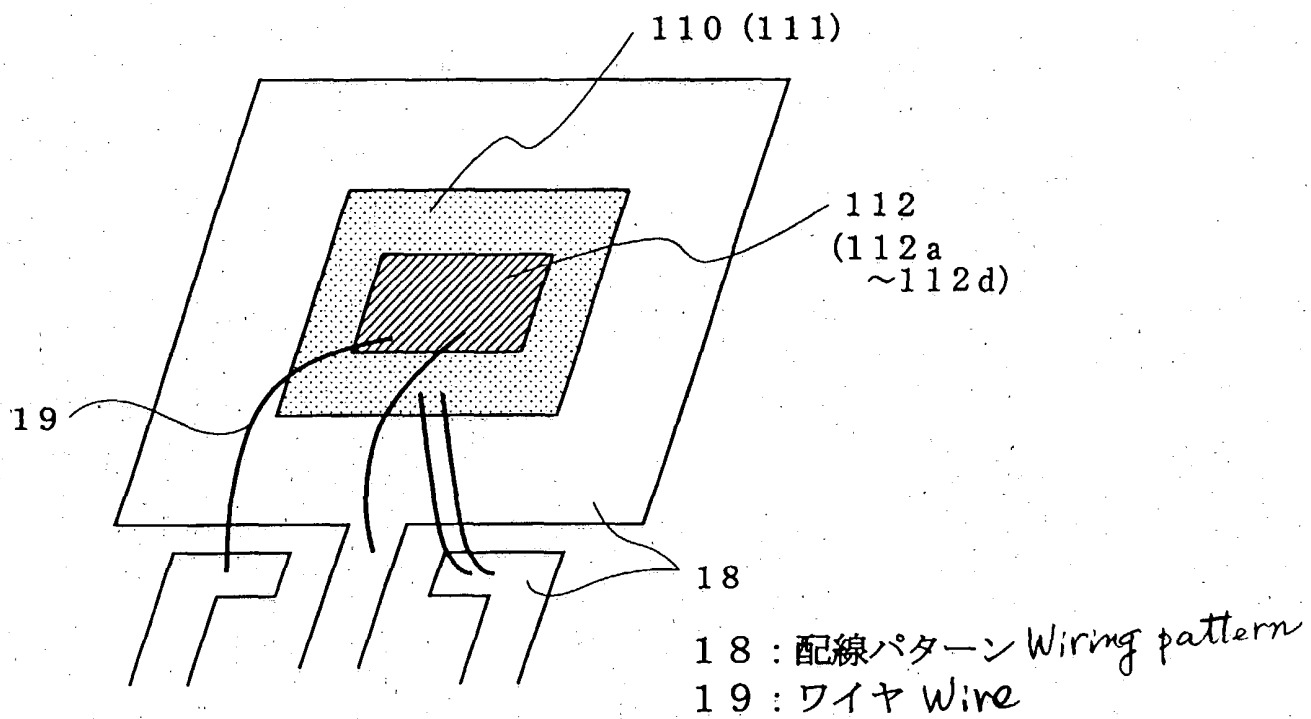
【図12】



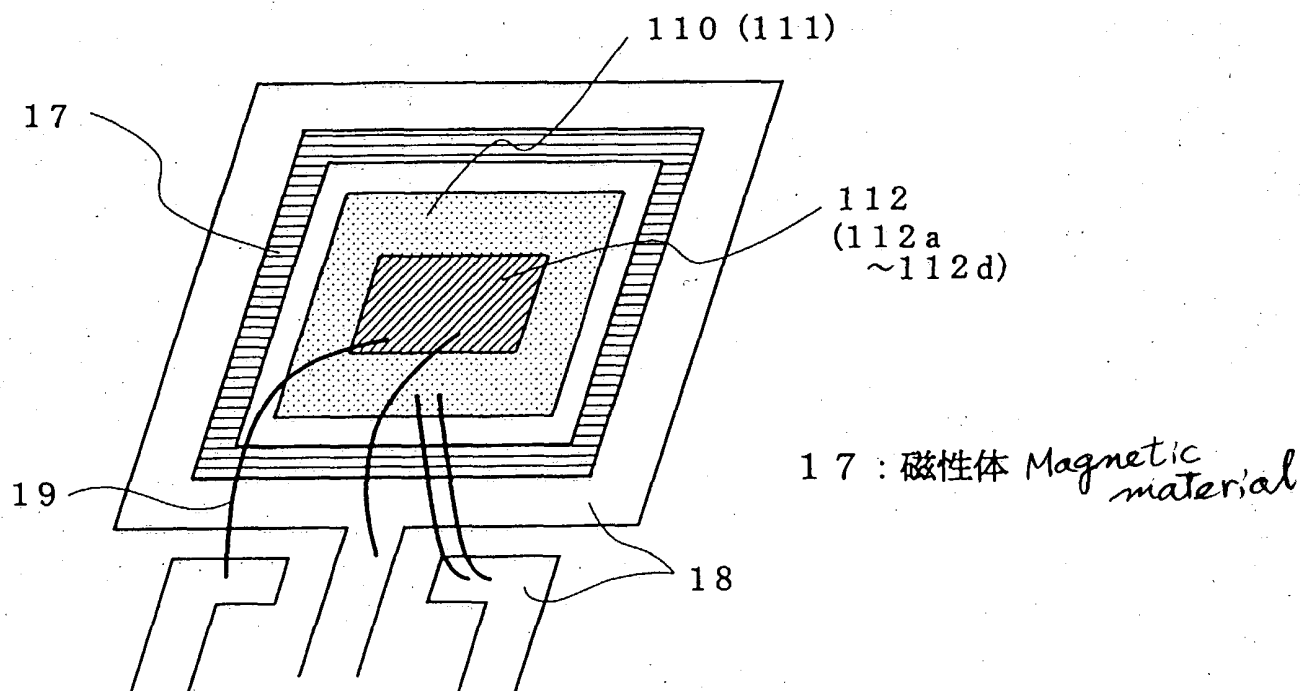
【図13】



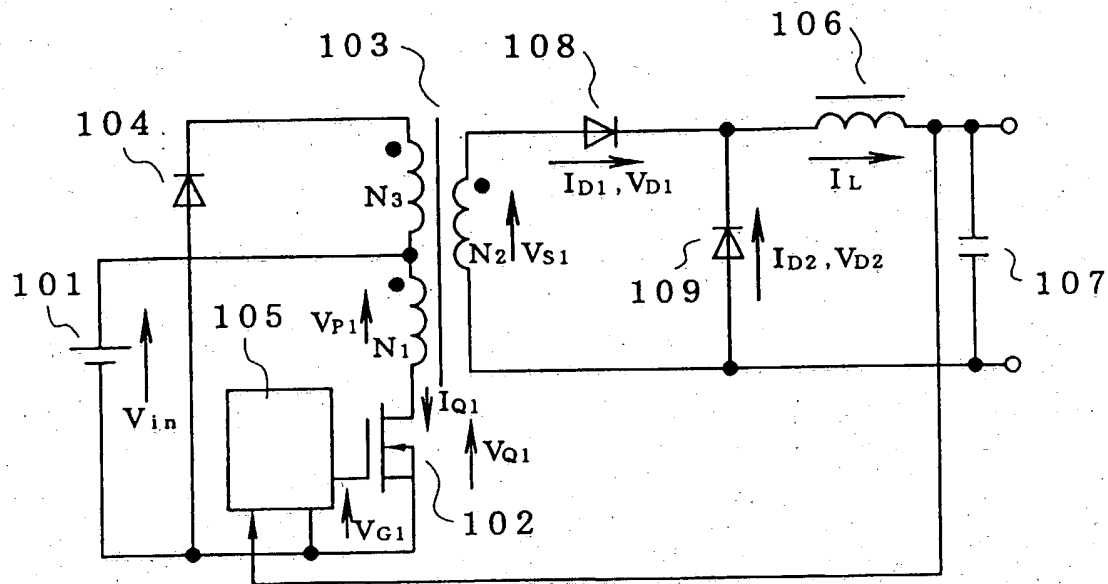
【図14】



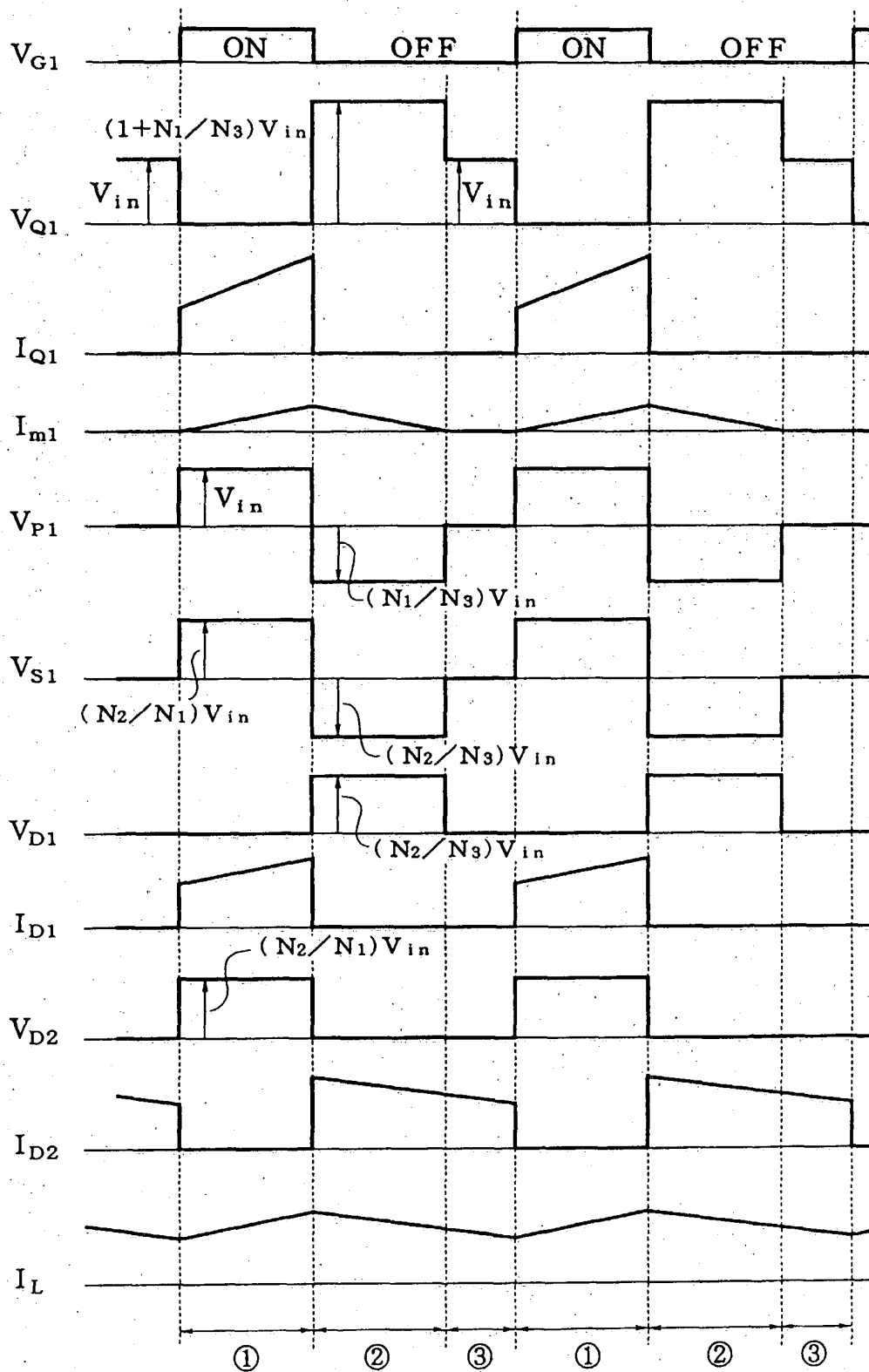
【図15】

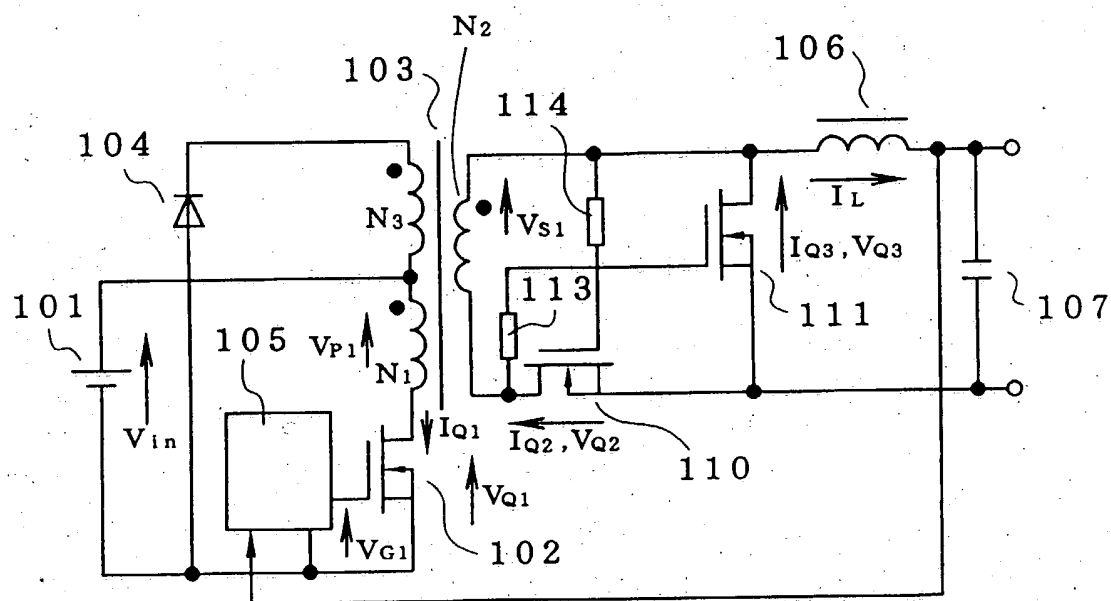


【図16】



【図17】





【図19】

